



Docket No.: SON-3057
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yoshitoshi Kida et al.

Application No.: 10/563,298

Confirmation No.: 1328

Filed: January 3, 2006

Art Unit: 2629

For: FLAT DISPLAY APPARATUS AND
INTEGRATED CIRCUIT

Examiner: R. L. Willis

COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant has received the Examiner's Statement of Reasons for Allowance with the Notices of Allowance and Allowability of December 7, 2009, in connection with the above-identified application. The Examiner therein provided a Statement of Reasons for Allowance regarding the claims.

In that regard, Applicant does not acquiesce to any inference or presumption that may be drawn from the Examiner's stated reasoning, and the issuance of the Examiner's Statement of Reasons for Allowance should not be construed as surrender by Applicant of any subject matter.

Dated: February 22, 2010

Respectfully submitted,

By

Christopher M. Tobin

Registration No.: 40,290

RADER, FISHMAN & GRAUER PLLC

1233 20th Street, N.W., Suite 501

Washington, DC 20036

(202) 955-3750

Attorney for Applicant